Serial No. 10/632,216 Inventors: Gerard Chauvel Title: Micro-Sequence Execution In A Processor New Sheet **START** Fig. 6A CONFIGURE A REGISTER AS AN INDIRECT REGISTER INDEX (IRI) 622 ADDRESS AN INDIVIDUAL VECTOR TABLE ENTRY VIA THE IRI 624-5 LOAD THE ENTRY FROM THE DATA CACHE 626-5 **END** 628-5 **START** Fig. 6B CONFIGURE A REGISTER AS AN INDIRECT REGISTER INDEX (IRI) 632-5 ADDRESS MULTIPLE VECTOR TABLE ENTRIES VIA THE IRI 634-5 LOAD THE ENTRIES FROM THE DATA CACHE UTILIZING A REPEAT LOOP OF INSTRUCTIONS COMPRISING AT LEAST ONE INSTRUCTION 636-5

638 -5

**END**